



## TEMPLATE FOR PROPOSAL UNDER DERRI

### User-Project Proposal:

User-Project Acronym	C-PoS FRT
User-Project Title	Combined FRT and Power Smoothing Control Method for Full-Converter Wind Turbines Employing Supercapacitor Energy Storage System
Main-scientific field	Wind Energy, Energy Management Control
Specific-Discipline	Control of Power Electronics

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Activity type and legal status* of Organization	Higher Education Institute
Position in Organization	Assistant Professor

\* Higher Education Institution (1) – Public research organization (2) – Private not-for-profit research organization (3) – Small or Medium size private enterprise (4) – Large private enterprise (5) – other (specify)

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Activity type and legal status* of Organization	Higher Education Institute
Position in Organization	PhD Candidate

\* Higher Education Institution (1) – Public research organization (2) – Private not-for-profit research organization (3) – Small or Medium size private enterprise (4) – Large private enterprise (5) – other (specify)

### (Repeat for all Users)

Date of submission	28/06/2012
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Re-submission	YES _____ NO <u>X</u> _____
Proposed Host TA Facility	TA6 <i>Fraunhofer Institute for Wind Energy and Energy System Technology</i>
Starting date (proposed)	15 <sup>th</sup> December

**Summary of proposed research (about ½ page)**

As wind plants increase in size and number they have a greater impact on the grid while they displace other conventional generation units. The capability of a wind turbine to withstand short-duration grid faults and other disturbances is referred to in the literature as fault ride-through (FRT) capability or low voltage ride-through (LVRT) capability. Most utility administrators have revised their grid codes in order to address the latest FRT requirements. Previous research has focused on the improvement of FRT capability as long as the generator is still connected to the grid. The existence of distributed generation units in the power system has consequences for the conventional protection devices, as they might become blind to overcurrents and short circuits. This research proposes the disconnection of the wind power unit when a fault occurs in the grid and the voltage does not recover within a time limit. The energy produced during the fault is stored in a Supercapacitor Energy Storage System, allowing the generator to stay functional for a period of time, despite being disconnected from the grid. In this way, the normal fault-clearing process can take place. Just after reconnection, the generators are able to supply the grid with power, ensuring reliability of the power system. Furthermore, the ESS is exploited to absorb the output power fluctuation due to wind speed deviation.

Experimental measurements from the test facilities will help the evaluation of the proposed Fault ride-through capability method as well as its ability to smoothen output power fluctuation due to wind speed variations. Longer fault duration can be tolerated, while the protection system clears the fault through the normal process. Furthermore, output power fluctuation is expected to be significantly reduced.

**State-of-the-Art (about 1 ½ page)**

**Low Voltage Ride Through requirements** have been developed by many utility grid administrators. Usually these characteristics are described through a voltage profile (LVRT reference curves) above which the wind turbine generators WTG should stay connected to the grid. Moreover, reactive current boosting during and after faults aims at supporting the grid voltage, for the purpose of enabling non-LVRT compliant power units and other decentralized generators to stay connected. Common point of all previous work is that the WTG should stay connected to the grid. [1-3]

**Fault Detection:** Forcing a wind turbine generator to stay connected could lead to undetected grid faults. The grid protection system might measure a lower current during the fault, due to the contributions of the generators to the fault current. As a result, the fault detection happens too slow or not at all. [4, 5]

**LVRT strategies:** In order to be able to meet the LVRT requirement, the control system and the hardware has to be modified to manage the energy produced by the WTG during the low-voltage event. Without energy management, the power produced by the WTG under fault conditions will remain within the electric machine, resulting in an increased mechanical speed. Options available to handle the energy that the grid cannot absorb depend on the type of WTG

used. The most commonly used LVRT solutions are listed as follows [6-9]:

1. In Fixed speed wind turbines (FSWT) using Squirrel Cage Induction generators a Static Var Compensator (SVC) or static synchronous compensator (STATCOM) at the point of common coupling (PCC) is used to support reactive power.
2. In Full Converter Driven WTG, dumping the energy in a resistor, which is typically connected to the dc bus, regulating the dc bus to within  $\pm 10\%$  of its nominal value;
3. Pitch angle control. The rate of pitch angle change is usually designed at a slow value (around  $10^\circ/\text{s}$ ) to prevent excursion of wind speed on the WT blades. The reaction of pitch controller is very slow in case of transient fault and ineffective when the mechanical power output should be reduced as fast as possible to prevent the speed acceleration.
4. In a DFIG, a commonly used solution is to shorten the rotor windings through a resistor (Crowbar) and operating the machine as a conventional induction machine;
5. Dumping the energy in an electrical storage device, for example, supercapacitor, superconducting magnetic energy storage, and batteries.

**Energy Storage Devices:** The most common implemented technologies for short-term energy storage are Lead-acid Batteries, Flywheels, Supercapacitors and Superconducting Magnetic Energy Storage (SMES). Flywheels and supercapacitors both offer similar characteristics and are both suitable for wind energy applications. They represent a storage device that has a high energy density and presents a good efficiency. In addition, they have a much higher life cycle than batteries. [10-13]

**Power smoothing:** In a full-converter driven WTGs power smoothing is achieved by controlling the dc link voltage level. This could be done either by raising the dc link voltage or by keeping it constant and storing the excess of power in an energy storage system. Consuming the excess of energy on a resistor is unadvisable but is used often. [14-17]. To produce a smoothed output power reference signal many technics (e.g. power ramp control, Low Pass Filter, Simple Moving Average) can be used.[18]

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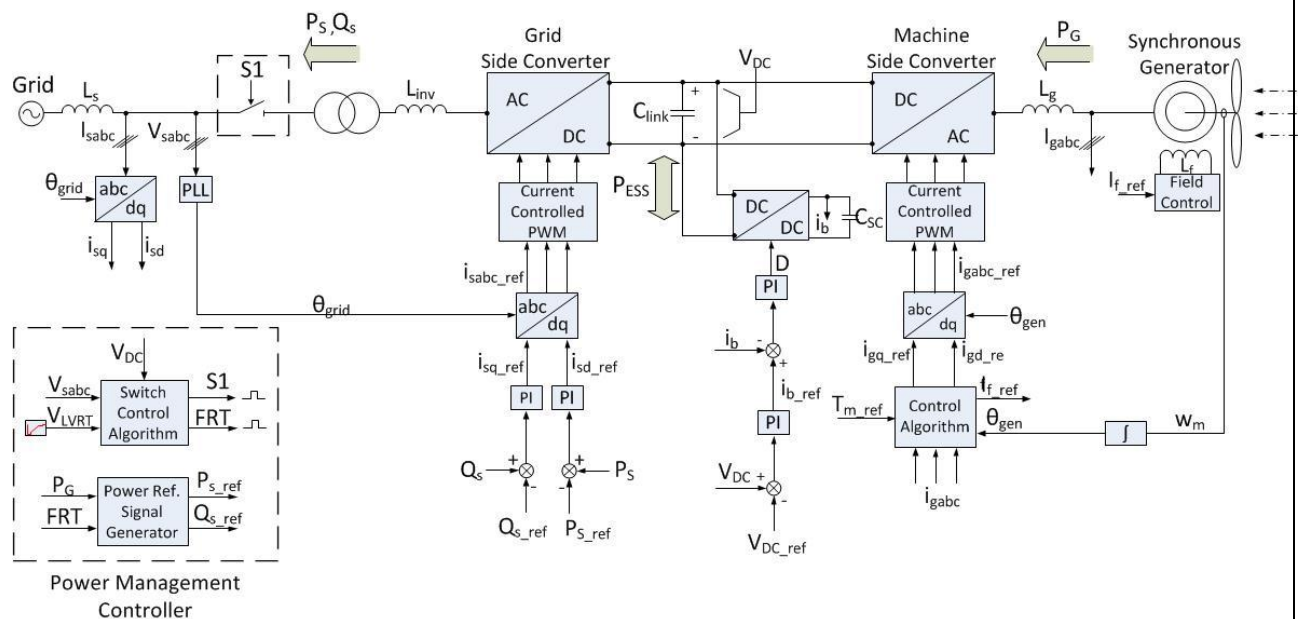
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**Detailed Description of proposed project : Objectives – Expected Outcome – Fundamental Scientific and Technical value and interest (2-3 pages)**

The aim of this proposal is the development of a combined Fault ride through and power smoothing method for full-converter wind turbines, employing a Supercapacitor Energy storage system. The Energy Storage System (ESS) is used in order to store the energy produced during severe grid faults. This allows the generator to stay functional for a period of time, despite being

disconnected from the grid. The key point of this method is the disconnection of the wind turbine driven generator in case the fault duration exceeds the maximum duration for which the generators should remain connected to the grid, according to the grid code requirements. The amount of energy that cannot be absorbed from the grid is handled through a control algorithm ensuring the safe function of the machine. Disconnection is performed in such a way as to allow the protection equipment of the grid to detect and clear the fault smoothly. Immediately after the fault clearance, the wind turbine generator reconnects to the grid and supplies active and reactive power without the necessity of a restart. Furthermore, the ESS can quickly absorb power fluctuations due to wind speed variation. Energy stored in the supercapacitor ESS is redirected to the grid-after the fault clearing- in order to improve output power quality.



The topology of the proposed Fault Ride-through control scheme and the power system is shown in the figure above. In a fully rated converter based WT, the generator is connected to the ac network through a full-sized back-to-back converter, which completely decouples the generator and the ac system. The converter system is composed of two voltage source converters, at the machine side (MSC) and the grid side (GSC) and a dc link capacitor. The main control objective of the MSC is to regulate the electromagnetic torque of the generator in order to control the shaft's speed. The control objective of the GSC is to regulate active power from/to the dc-link and the reactive power exchanged between the GSC and the power grid. The ESS is connected to the common DC link capacitor via a bidirectional half-bridge buck/boost DC/DC converter.

Both in a variable-speed squirrel cage induction generator (SCIG) and in a synchronous generator with external excitation, full-scaled converters are required to adjust the rotational speed of the generator. For the squirrel cage induction generator (SCIG) an indirect vector control in the rotor flux oriented frame is implemented. Field-oriented control is also implemented to control the synchronous generator with external excitation. The MSC control block takes as input the 3-phase currents and voltages at the machine side.

The control objective of the GSC is to regulate active and reactive power exchanged between the GSC and the power grid. This is achieved by means of current regulation in a synchronously rotating reference frame. The power reference signals for the active and reactive power controllers are obtained from the Power Management Controller (PMC).



A bidirectional DC/DC converter is used to control the charging and discharging of the ESS according to the level of the dc link voltage and the operating mode (FRT, Power Smoothing). The DC/DC control block takes as inputs the dc-link voltage, the supercapacitor voltage and the supercapacitor current.

A power management controller (PMC) is fed by several signals coming from the Power system and performs the following operations:

- A. Detection of symmetrical voltages sags. In this study it is supposed that a fault occurs on the grid if a voltage variation greater than  $\pm 10\%$  is detected, according to standard EN 50160.
- B. Control the state of switch S1. The state of the switch is determined by constantly measuring the grid voltage magnitude at the point of common connection (PCC) and comparing it with the low-voltage ride through reference curve. In case the grid voltage falls below the LVRT reference curve, switch S1 opens to disconnect the WTG from the grid.
- C. Generates the power reference signals for the grid side converter. The reference signals are generated according to the operation mode of the system and the state of charge of the supercapacitor.
- D. Control of the ESS charging/discharging.
- E. Resynchronization. Disconnecting the WTG/PCS from the grid implies the need to develop a fast and efficient resynchronization procedure. By employing a hysteresis-band current controlled Voltage Source Converter (VSC) at the grid side, the synchronization procedure is simplified. Inserting a delay in the reclosing of the switch smooth reconnection with almost no transient should be achieved

Indicative type of TA infrastructure used to fulfill the objectives:

- 2 types of generators
- 1 DC machine
- 2 full controllable 3-phase inverters
- 1 DC/DC buck/boost converter
- 1 dc link capacitor
- 1 Supercapacitor
- 3-phase sinusoidal grid with controlled amplitude
- 1 controllable switch

Number of test carried out:

1. The performance of the proposed FRT control scheme is tested when riding through different levels grid voltage disturbances. Two cases are examined:
  - The level of symmetrical voltage dip is above the LVRT reference curve and does not cause disconnection of the generator. Limited power is transferred to the grid, depending on the voltage dip level. The rest of energy is stored in the ESS to avoid rising of the dc link voltage.
  - The level of voltage dip causes disconnection of the generator. The total amount of generated power is stored in the ESS.

In both cases, different fault time periods are tested

2. Testing the ability of the system to achieve smooth resynchronization to the grid, after fault clearance, when a hysteresis-band current controlled VSC is used.
3. Sinusoidal variation of the mechanical torque in order to simulate wind speed fluctuation. The variation should cause a controlled (frequency, magnitude) sinusoidal variation of the



generated power. The ability of the system to smoothen middle frequency (0,01 -1Hz) power fluctuation is tested.

4. Testing two different types of generator in order to prove that the proposed FRT scheme is independent on the type of the used WTG.

#### **Originality and Innovation of proposed research – Broader Impact (1-2 pages)**

Disconnecting the Wind Turbine Generator (WTG)/Power Conversion System (PCS) from the grid under certain conditions allows the proposed FRT method to comply with the latest LVRT grid code requirements. Furthermore, it can be used in a power network where traditional protection devices are used, as it disconnects the WTG from the grid, allowing the protection system to clear the fault safely. This will facilitate the integration of wind parks in the total power production.

Moreover, it takes advance of the supercapacitor in order to smoothen the generated output power. A complete algorithm is developed to maximize the Fault Ride Through capability of the system and at the same time provide the grid with quality power.

After the completion of the test measurements, it will be proved that the proposed model can be used with any type of converter driven ac generators

Real time measurements will reveal many practical problems that may arise in the process and will identify possible restrictions from the hardware devices such as metering devices, controllers etc.

#### **Proposed Host TA Infrastructure/Installation – Justification (about one page)**

The Host Infrastructure should afford all the required equipment to implement the proposed Fault Ride-Through and power smoothing method. Two types of inverter driven generators (Synchronous and Asynchronous) should be available in order to prove that the proposed control scheme is independent on the type of the generator used. Two back-to-back inverters (with a dc-link capacitor) rated at the generators power are required. The inverters should be fully controllable in order to implement the proposed control scheme. A second capacitor which will be used as energy storage device is needed as well as a DC/DC converter to control the charging and discharging of the capacitor. A controlled power source that will simulate the grid voltage dips should be available. Finally, a controllable switch is needed in order to connect/disconnect the WTG. Finally, it would be desirable the ability to download the control algorithm from PSIM to the inverter microprocessors, as the proposed control scheme is simulated in PSIM software.

The test facility should be also equipped with a high speed data acquisition system with a time step of a few  $\mu$ s. Measuring devices should be available providing the ability to monitor currents, voltages active and reactive power at desired nodes. Finally suitable protection devices should be available in order to ensure that no operational limit is surpassed.

From the available host infrastructure, Fraunhofer Institute for Wind Energy and Energy System Technology seems to be suitable for the proposed study.

#### **Synergy with ongoing research (about ½ page)**

No other similar project is ongoing in the Electrical Machines Laboratory.

**Dissemination – Exploitation of results (about ½ page)**

Once the test measurements are completed, a report with the obtained results will be exported. These results will be included in our final work where the test measurements will be used to support the theoretical analysis. Part of the results of this project will be published in international conferences and scientific journals.

**Time schedule (about ½ page)**

Tasks	Time
1. Familiarizing with the available equipment. The proposed topology will be built and the all the tests will be designed in detail.	First Week
2. The performance of the proposed FRT control scheme is tested when an induction generator is riding through different grid voltage disturbances. Several test measurements will be carried out for various fault time periods and level of grid voltage dip.	Second Week
3. The same scenarios as in 2 will be tested when a synchronous generator is used.	Second Week
4. In cases 2 and 3 the ability to smoothly re-synchronize to the grid (after disconnection from the grid) will be tested.	Second Week
5. The effectiveness of the proposed power smoothing method will be tested. Various power fluctuation scenarios will be considered. The quality of the generated power will be evaluated for various storage capacities and power fluctuation conditions.	Third Week
6. By the end of the experiments a complete test report will be exported. The experimental results will be compared to the theoretical analysis and the software simulation results.	Third Week

**Description of the proposing team (as long as needed)**

**Spyros I. Gkavanoudis** was born in 1983. He received the Dipl. in electrical engineering from the Aristotle University of Thessaloniki, Thessaloniki, Greece, in 2008. Currently, he is a Ph.D student at the Department of Electrical and Computer Engineering, Aristotle University of Thessaloniki. His research interests are in the fields of power electronics, distributed generation and renewable energy sources.

**Charis S. Demoulias** was born in 1961. He received the Dipl. and Ph.D. degrees in electrical



engineering from the Aristotle University of Thessaloniki, Thessaloniki, Greece, in 1984 and 1991, respectively. Currently, he is an assistant Professor in the Electrical Machines Laboratory, Department of Electrical and Computer Engineering, Aristotle University of Thessaloniki. His research interests are in the fields of power electronics, harmonics, electric motion systems, and renewable energy sources. His main published papers are the following:

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The **Electrical Machines Laboratory** belongs to the Energy Section of the Electric and Computer Engineering department of the Polytechnic School of Aristotle University of Thessaloniki and was founded by Professor Ioannis Xypteras in 1984.

The staff of the Laboratory includes two Assistant Professors, two assistants and five Post-Graduate Students. The **Electrical Machines Laboratory** has two main facilities, one in Building D' 5th floor (110 sq.m.) and one in Building E', 2nd floor (100 sq.m.).

The laboratory serves educational and research needs in the fields of electrical machines (design, modelling, analysis of operation), power electronics (design, construction and design, power converters) drive systems and electrical machinery (control of electric motors and generators, inverters, solar power, wind turbine control systems, etc.).

The Electrical Machines Laboratory serves for:

- Laboratory exercises for six semester courses of the undergraduate studies,
- Laboratory diploma theses
- Doctoral theses
- Research papers and research programs
- Providing services to individuals and organizations

The aim of the research department of the laboratory is to produce new knowledge, which can be incorporated in the educational process and promote the science of electrical engineering.



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